

Notice of References Cited	Application/Control No. 10/561,135		Applicant(s)/Patent Under Reexamination VORBACH ET AL.	
	Examiner IDRISS N. ALROBAYE		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,868,476	03-2005	Rosenbluth et al.	711/108
	B	US-			
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	John Reid Hauser "Augmenting a Microprocessor with Reconfigurable Hardware", University of California, Berkeley, Fall 2000.
	V	Muthu Venkatachalam; Prashant Chandra; Raj Yavatkar "A highly flexible, distributed multiprocessor architecture for network processing", April 5, 2003
	W	John R. Hauser, "The Garp Architecture", University of California at Berkeley, October 1997
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.